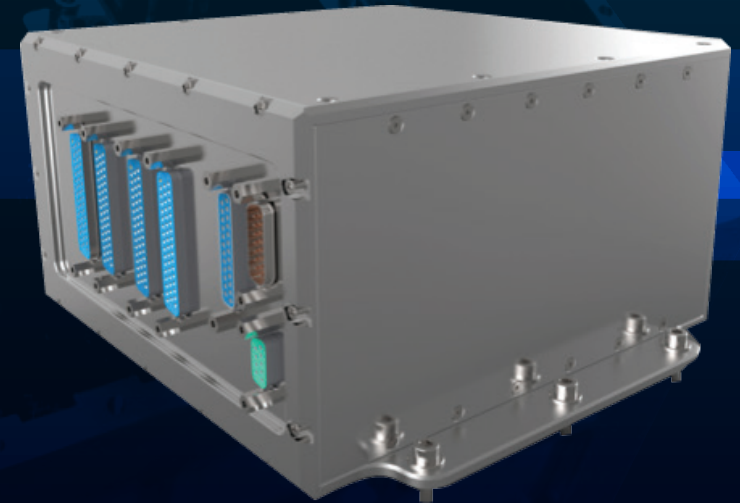
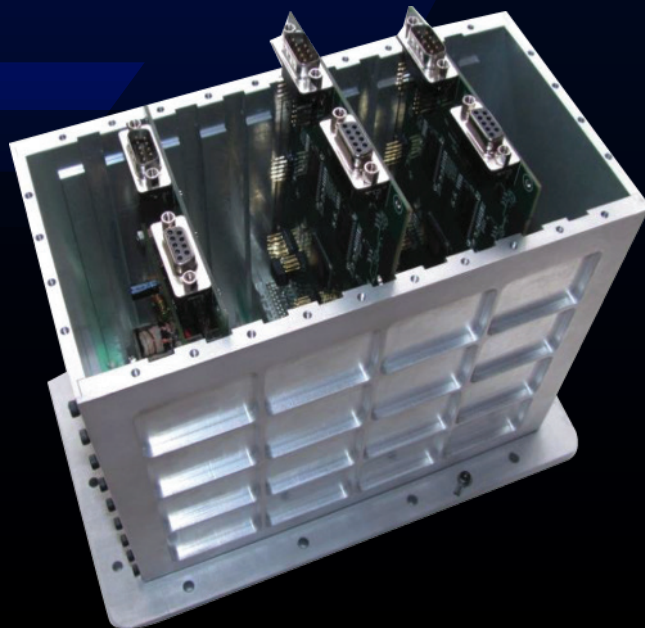


The background of the image is a dark blue gradient. A faint, stylized illustration of a rocket ship is visible, angled upwards from the bottom left towards the top right. The rocket has a cylindrical body with various details like windows and structural elements. The text 'SPACE' is positioned above 'CORE' in a light blue, sans-serif font. The word 'CORE' is significantly larger and features a unique design where the 'E' is replaced by three horizontal bars.

SPACE
CORE

AEROSPACE

- Flight Control Computers
- Mission Computers
- Payloads Computers
- Hi-Rel Embedded Computers

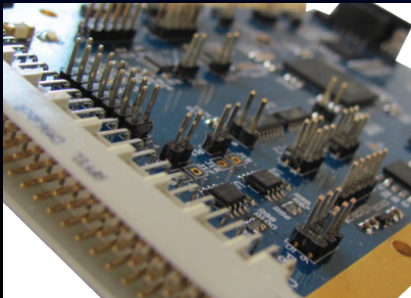
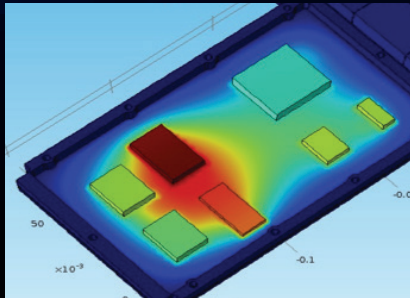
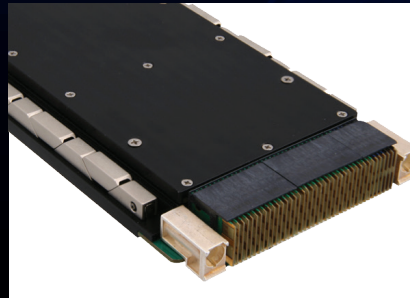
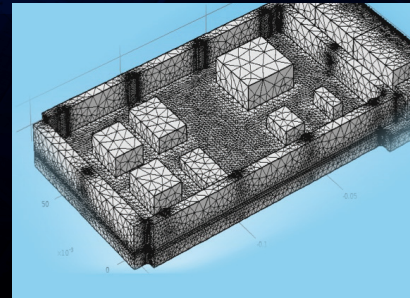
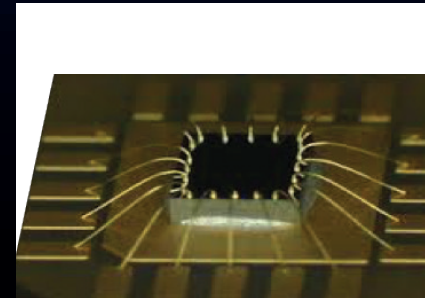


HIGH RELIABILITY

Thermal Stress, Shock, Vibrations, ←
Hi-Rel Applications, Vacuum App, ←
HIGH Temperature App ←

HARDWARE RESEARCH & DEVELOPMENT

- Electronics, Thermal & Structural
- Signal Integrity
- Thermal Shock
- Vibration Analysis
- FEM Analysis
- Radiation Tolerant
- Hard Rad Devices
- Failure Analysis, FMEA/FMECA/SEE
- Microelectronics / KET

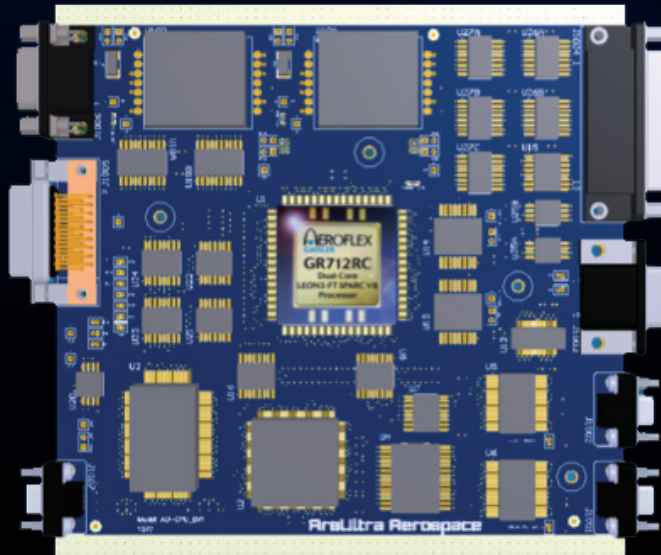
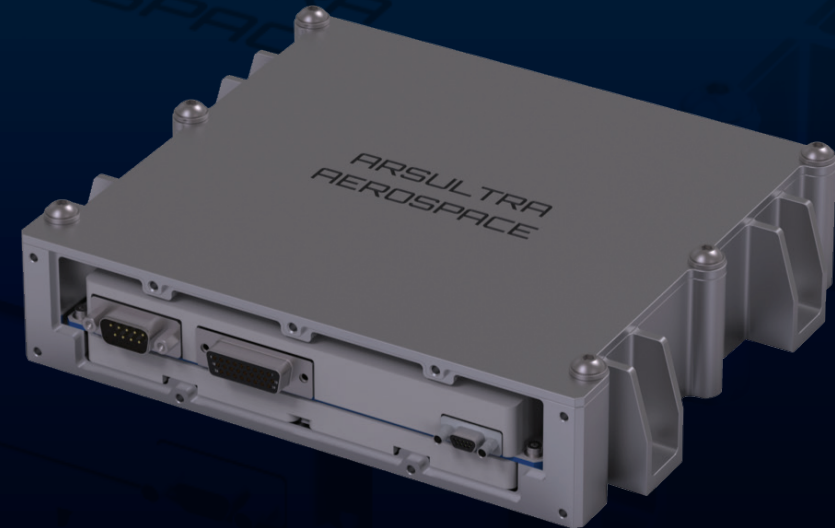


RAD-HARD HIGH Reliability LEON3 CPU

For High Reliability Satellites

155 mm

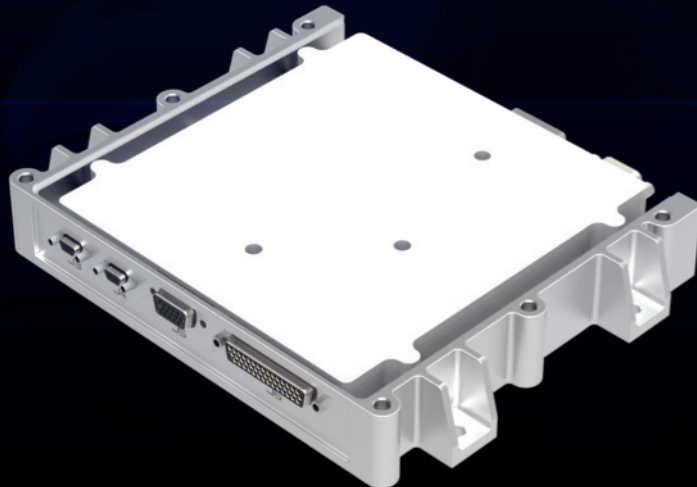
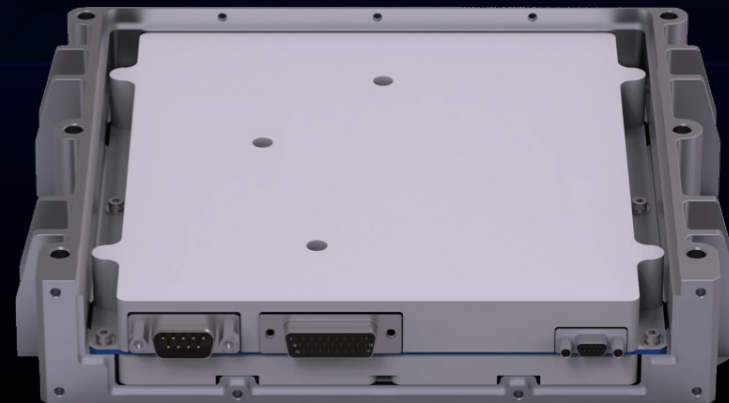
150 mm



ITAR FREE LEON3

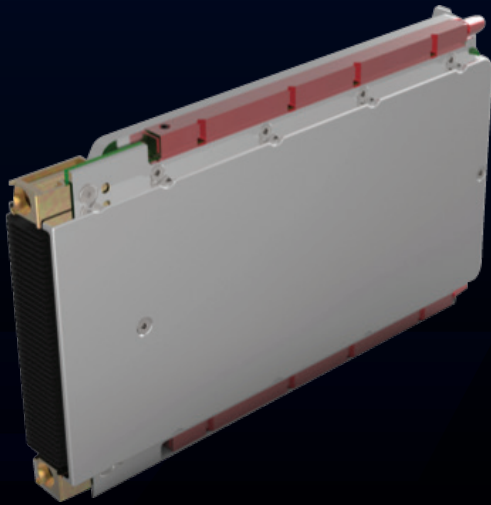
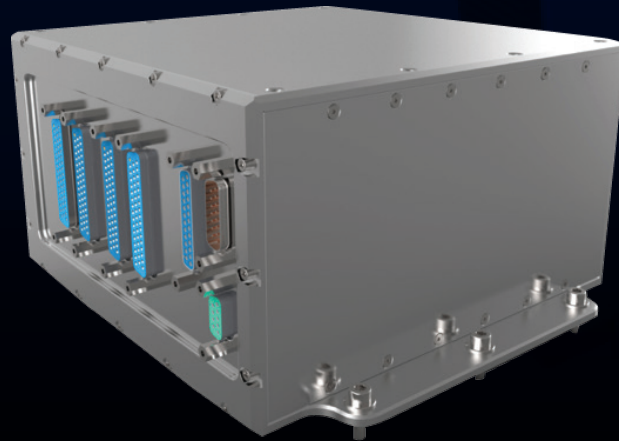
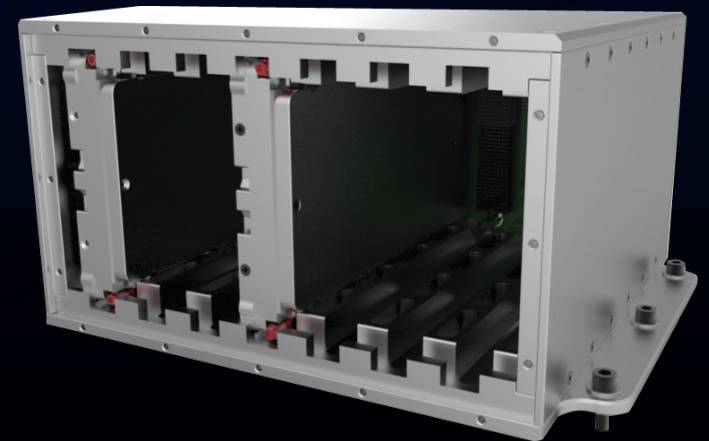
Fault Tolerant CPU for Space Applications

Extremely Lightweight
Rugged Enclosure



OpenVpx ITAR FREE LEON3

Fault Tolerant CPU for Space Applications



CUBESAT

Form-Factor Hi-Rel OBC

- Fly Angela is an Onboard Computer for multiple applications.
- It has been equipped with automotive grade chips used in power and memory components.
- Its design of PC104-PLUS format is ideal for CubeSat applications.





HIGH Throughput Processing CPU Board

The crypto coprocessor

In addition to the standard execution units, a tightly coupled accelerator allowing to speed-up by a typical 10X to 20X factor the processing of specific algorithms, such as:

- Symetric cryptography with AES and DES encryption/decryption support
- Secure Hash SHA1, SHA2 (224/256, 384/512)
- Checksum, from CRC1 to CRC64
- Galois Counter Mode (GCM) authentication
- Carry-Less Multiply primitives



256 CORES 600 MHz
Single and Dual FPU precisión

The crypto co-coprocessor is shared by two cores making a total of 128 instances on a full MPPA2-256 chip.

A dark blue background featuring a faint, stylized image of a space shuttle launch. The shuttle is angled upwards from the bottom left towards the top right. The word "USA" is visible on the side of the shuttle's nose cone. The overall aesthetic is clean and modern, with a focus on space exploration.

SPACE CORE

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www.space-core.space

sales@space-core.space